

METHOD AND SYSTEM FOR PERFORMING A MEMORY-MODE WRITE TO CACHE

ABSTRACT OF THE DISCLOSURE

A method of writing to cache including initiating a write operation to a cache. In a first operational mode, the presence or absence of a write miss is detected and if a write miss is absent, writing data to the cache and if a write miss is present, retrieving the data from a further memory and writing the data to the cache based on least recently used logic. In a second operational mode, the cache is placed in a memory mode and the data is written to the cache based on an address regardless of whether a write miss is present or absent.